

FIGS in 10/19/01
PC

7/6/DIC
8/20/01
Surber

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE



Art Unit : 2814
Examiner: Mr. Phat X. Cao

In re PATENT APPLICATION of

Applicants : Yasutaka SAKAINO, et al.

Appln. No.: 09/229,628

Filed : January 13, 1999

For : SEMICONDUCTOR INTEGRATED
CIRCUIT

Att'y Dkt. : 32014-141666 (Formerly OKI 4646.01)

**PROPOSED DRAWING
CHANGES**

February 13, 2001

Assistant Commissioner for Patents
Washington, D.C. 20231

Sir:

Approval of the proposed drawing changes marked in red ink on the copies of Figures 1
to 6-B attached hereto is respectfully requested.

Respectfully submitted,

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